

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-602001	Application No. 09/994,574
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant William R. Wheeler et al.	
		Filing Date November 26, 2001	Group Art Unit 2123

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U.S. Patent Documents

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Examiner Initial	Design ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
R	AA	4,970,664	11/1990	Kaiser, et al.			
R	AB	5,212,650	05/1993	Hooper, et al.			
R	AC	5,267,175	11/1993	Hooper			
R	AD	5,278,769	01/1994	Bair, et al.			
R	AE	5,297,053	03/1994	Pease, et al.			
R	AF	5,301,318	04/1994	Mittal			
R	AG	5,384,710	01/1995	Lam, et al.			
R	AH	5,493,507	02/1996	Shinde, et al.			
R	AI	5,544,067	08/1996	Rostoker, et al.			
R	AJ	5,603,015	02/1997	Kurosawa, et al.			
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R	AL	5,673,198	09/1997	Lawman, et al.			
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R	AO	5,717,928	02/1998	Campmas, et al.			
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R	AQ	5,809,283	09/1998	Vaidyanathan, et al.			
R	AR	5,831,869	11/1998	Ellis, et al.			
R	AS	5,903,469	05/1999	Ho			
R	AT	5,937,190	08/1999	Gregory, et al.			
R	AU	5,974,242	10/1999	Damarla, et al.			
R	AV	6,077,304	06/2000	Kasuya			
R	AW	6,161,211	12/2000	Southgate			
R	AX	6,178,541	01/2001	Joly, et al.			
R	AY	6,208,954	03/2001	Houtchens			
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R	AAA	6,234,658	05/2001	Houldsworth			

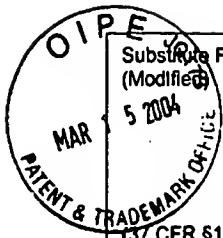
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 U.S. Department of Commerce
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 Attorney's Docket No.
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**Information Disclosure Statement
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(37 CFR §1.98(b))

 Applicant
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R	ACC	6,360,356	03/2002	Eng			
R	ADD	6,381,563	04/2002	O'Riordan, et al.			
R	AEE	6,438,729	08/2002	Ho			
R	AFF	6,473,885	10/2002	Wallace			
R	AGG	6,490,545	12/2002	Peng			
R	AHH	6,505,328	01/2003	Van Ginneken, et al.			
R	AII	6,516,456	02/2003	Garnett, et al.			
R	AJJ	6,519,742	02/2003	Falk			
R	AKK	6,546,528	04/2003	Sasaki, et al.			
R	ALL	6,591,407	07/2003	Kaufman, et al.			
R	AMM	2002/0038447	03/2002	Kim, et al.			
R	ANN	2002/0059054	05/2002	Bade, et al.			
R	AOO	2002/0112221	08/2002	Ferreri, et al.			
R	APP	2002/0138244	09/2002	Meyer			
R	AQQ	2003/0004699	01/2003	Choi, et al.			
R	ARR	2003/0036871	02/2003	Fuller, et al.			
R	ASS	2003/0177455	09/2003	Kaufman, et al.			

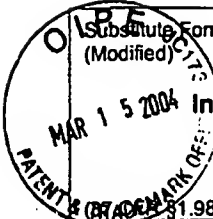
Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
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R	ATT	0 404 482	12/1990	EPO				
R	AUU	0 433 066	06/1991	EPO				
R	AVV	0 901 088	03/1999	EPO				
R	AWW	1 065 611	01/2001	EPO				
R	AXX	03-225523	10/1991	Japan				
R	AYY	08-314892	11/1996	Japan				

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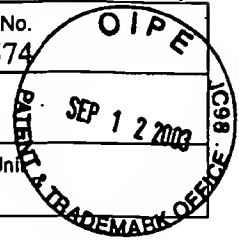
Foreign Patent Documents or Published Foreign Patent Applications								
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							Yes	No
R	AZZ	WO 98/37475	08/1998	WIPO				
R	AAAA	WO 98/55879	12/1998	WIPO				
R	ABBB	WO 99/39268	08/1999	WIPO				
R	ACCC	WO 00/65492	11/2000	WIPO				

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Examiner Initial	Desig. ID	Document
R	ADDD	Gassenfeit, E. H., "Control System Design Realization via VHDL-A: Requirements", Proceedings of the 1996 IEEE International Symposium on Computer-Aided Control System Design, September 15, 1996, pp. 282-285.
R	AEEE	Lahti, et al., "SADE: a Graphical Toll for VHDL-Based System Analysis", 1991 IEEE International Conference on Computer-Aided Design, November 11, 1991, pp. 262-265.
R	AFFF	Lin, et al., "A Goal Tree Based High-Level Test Planning System for DSP Real Number Models", 1998 Proceedings of International Test Conference, October 18, 1998, pp. 1000-1009.
R	AGGG	Maxfield, C., "Digital Logic Simulation: Event-Driven, Cycle-Based, and Home-Brewed", <i>Electrical Design News</i> , 41(14):129-136 (1996).
R	AHHH	NB84035598, "Binary Relational Schema to Model Structured LSI Design", IBM Technical Disclosure Bulletin, Vol. 26, No. 10B, March 1984, pp. 5598-5601.
R	AIII	NN7807629, "Functional Oriented Symbolic Macromodeling Algorithm", IBM Technical Disclosure Bulletin, Vol. 21, No. 2, July 1978, pp. 629-631.
R	AJJJ	NN9407481, "Functional Modeling using object Collaboration Diagram", IBM Technical Disclosure Bulletin, Vol. 37, No. 7, July 1994, pp. 481-486.
R	AKKK	Parlakkbilek, et al., "A Multiple-Strength Multiple-Delay Compiled-Code Logic Simulator", <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 12(12):1937-1946 (1993).
R	ALLL	Su, Stephen, "An Interactive Design Automation System", <i>Proceedings of the 10th Design Automation Workshop on Design Automation</i> , pp. 253-261, June 1973
R	AMMM	Yli-Pietila, et al., "The Design and Simulation of Complex Multitechnology Systems", IEEE International Conference on Systems Engineering, August 9, 1990, pp. 474-477.

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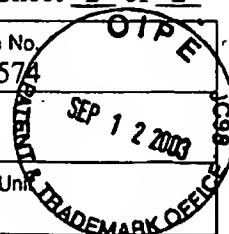
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		Filing Date November 6, 2001	Group Art Unit



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Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	
R	AA	5,598,347	01/28/1997	Iwasaki			
R	AB	RE 38,059	04/01/2003	Yano et al.			SEP 15 2003
R	AC	6,574,787	06/03/2003	Anderson			Technology Center 2100
R	AD	6,539,536	03/25/2003	Singh et al.			
R	AE	6,523,156	02/18/2003	Cirit			
R	AF	6,477,688	11/05/2002	Wallace			
R	AG	6,440,780	08/27/2002	Kimura et al.			
R	AH	6,438,731	08/20/2002	Segal			
R	AI	6,421,816	07/16/2002	Ishikura			
R	AJ	6,401,230	06/04/2002	Ahanessians et al.			
R	AK	6,381,565	04/30/2002	Nakamura			
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R	AO	6,275,973	08/14/2001	Wein			
R	AP	6,216,256	04/10/2001	Inoue et al.			
R	AQ	6,044,211	03/28/2000	Jain			
R	AR	5,892,682	04/06/1999	Hasley et al.			
R	AS	5,841,663	11/24/1998	Sharma et al.			
R	AT	5,724,250	03/03/1998	Kerzman et al.			
R	AU	5,685,006	11/04/1997	Shiraishi			
R	AV	5,663,662	09/02/1997	Kurosawa			
R	AW	5,568,397	10/22/1996	Yamashita et al.			
R	AX	5,475,605	12/12/1995	Lin			
R	AY	5,287,289	02/15/1994	Kageyama et al.			
R	AZ	4,703,435	10/27/1987	Darringer et al.			
	AAA						

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Foreign Patent Documents or Published Foreign Patent Applications							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Success	Translation
							Yes No
R	ABB	JP 2001068994	03/16/2001	Japan			Abstract only
R	ACC	EP 720233	07/03/1996	European Patent			X
R	ADD	JP 07049890	02/21/1995	Japan			Abstract only
R	AEE	JP 58060559	04/11/1983	Japan			Abstract only
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R	AHH	Kutzschebauch, "Efficient logic optimization using regularity extraction", Proceedings of 2000 International Conference on Computer Design, 17 September 2000, pp. 487-493.
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	AJJ	

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(37 CFR §1.98(b))

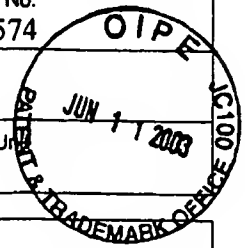


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R	AE	5,666,289	09/1997	Watkins			
R	AF	6,066,179	05/2000	Allan			
R	AG	5,892,678	04/1999	Tokunoh et al.			
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R	AV	5,852,564	12/1998	King et al.			
R	AW	6,106,568	08/2000	Beausang et al.			
R	AX	6,219,822	04/2001	Griestede et al.			
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Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
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R	AEE	6,272,671	08/2001	Fakhry			
R	AFF	6,260,179	07/2001	Ohsawa et al.			
R	AGG	6,117,183	09/2000	Teranishi et al.			
R	AHH	5,889,677	03/1999	Yasuda et al.			
R	AII	6,298,468	10/2001	Zhen			
R	AJJ	6,477,689	11/2002	Mandell et al.			
R	AKK	6,480,985	11/2002	Reynolds et al.			
R	ALL	6,519,755	02/2003	Anderson			
R	AMM	6,152,612	11/2000	Liao et al.			
R	ANN	2002/0023256	02/2002	Seawright			
R	AOO	6,053,947	04/2000	Parson			
R	APP	6,135,647	10/2000	Balakrishnan et al			
R	AQQ	2003/0016246	01/2003	Singh			
R	ARR	2003/0016206	01/2003	Taitel			
R	ASS	6,233,540	05/2001	Schaumont et al.			
R	ATT	6,487,698	11/2002	Andreev et al.			
R	AUU	6,311,309	10/30/2001	Southgate			
R	AVV	2002/0166100	11-2002	Meding			
R	AWW	US 5,629,857	05-1997	Brennan			
R	AXX	2002/0046386	04-2002	Skoll et al.			
R	AYY	6,401,230	06/2002	Ahanessians et al.			
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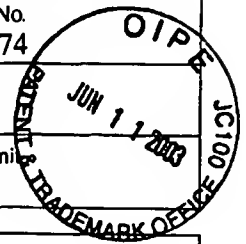
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Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	ADDD							
	AEEE							
	AFFF							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
R	AGGG	Foley et al., "An Object Based Graphical User Interface for Power Systems", IEEE Transactions on Power Systems, Vol. 8, No. 1, February 1993, pp. 97-104.
R	AHHH	Pedram et al., "Floorplanning with Pin assignment", 1990 IEEE International Conference on Computer-Aided Design, 11 November 1990, pp. 98-101.
R	AIII	Renoir, HDL Design Datasheet, Mentor Graphics, 1-8, 1999.
R	AJJJ	Mentor Graphics Corporation, Renoir™ With HDL2Graphics™, pp. 1-6, 1998, Oregon.
R	AKKK	Mentor Graphics Corporation, Renoir HDL Design Datasheet, pp. 1-2, 1999, Oregon.
R	ALLL	Computer Design, "AFTER HARD KNOCKS, CYCLE-BASED SIMULATORS STAND THEIR GROUND". http://www.computer-design.com/Editorial/1996/10/ASIC/after.html , accessed on August 23, 2001, pages 1-5.
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(37 CFR §1.98(b))

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							Yes	No
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	AM							
	AN							
	AO							
	AP							

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<i>R</i>	AR	Mentor Graphics Corporation, Renoir HDL Design Datasheet, 1999, Oregon
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	AT	

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